

FIG. 1

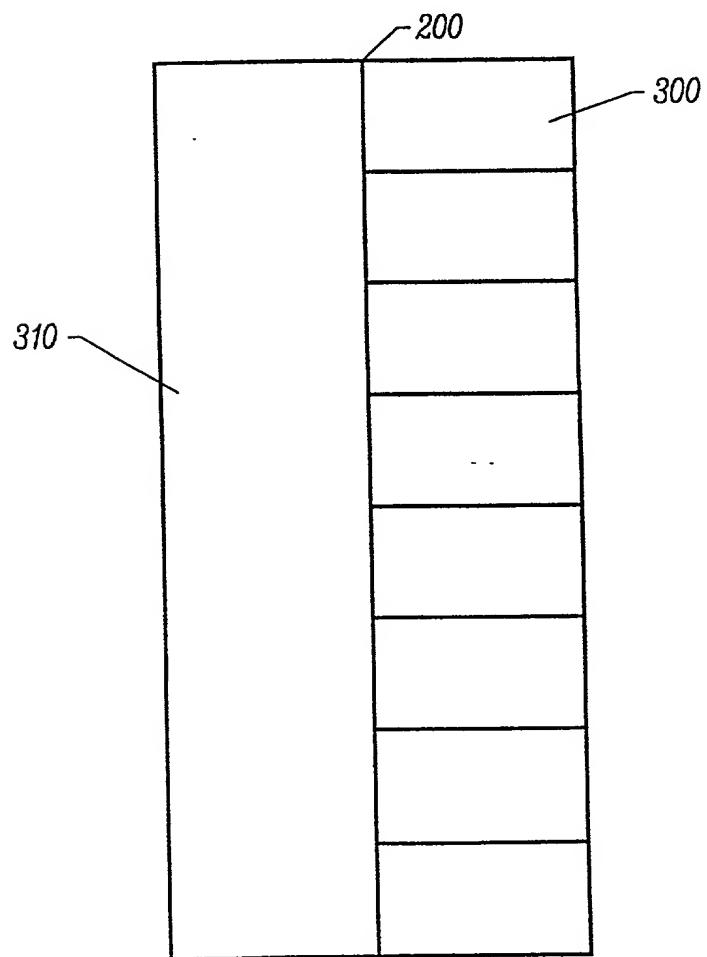


FIG. 3

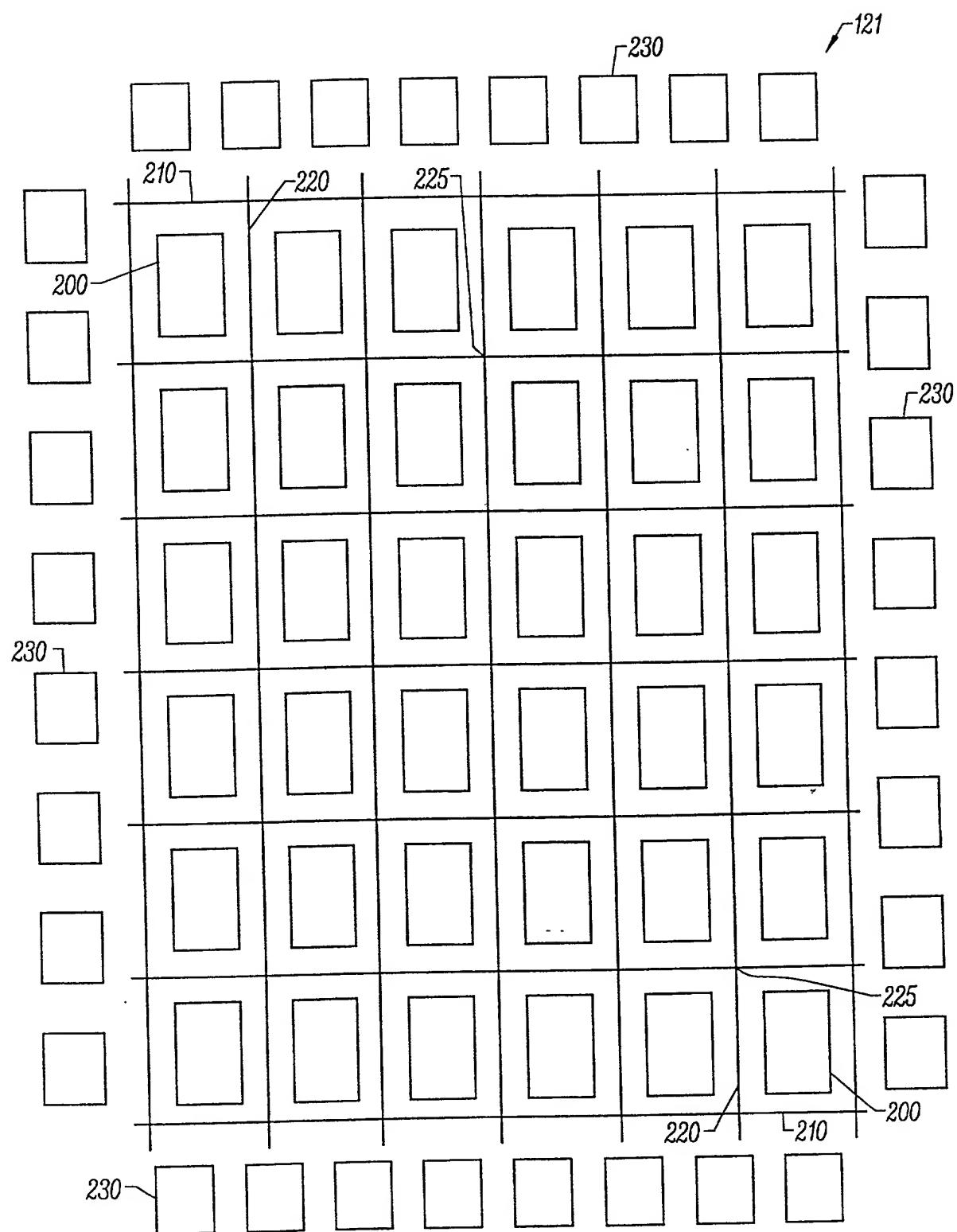


FIG. 2

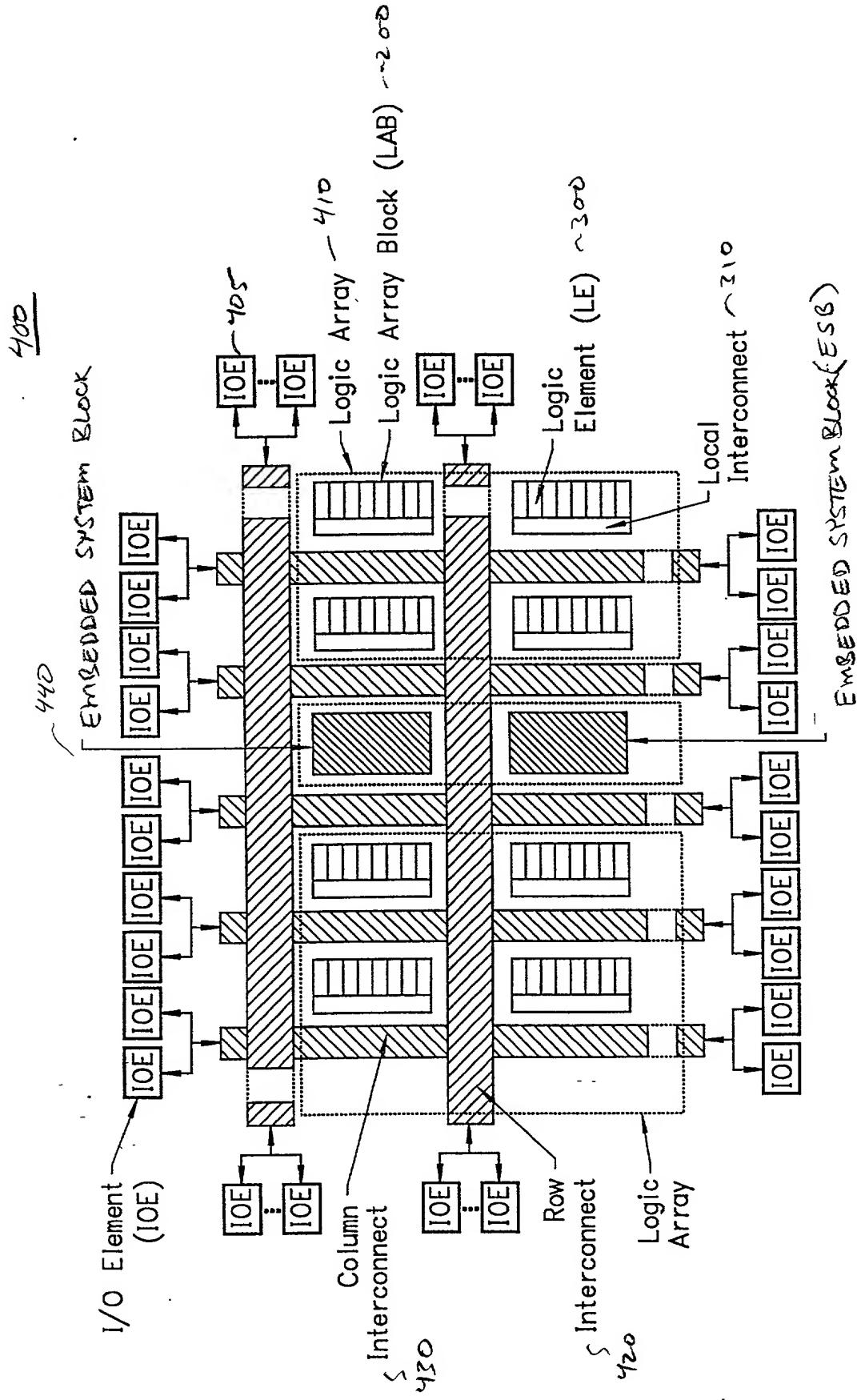


FIG. 4

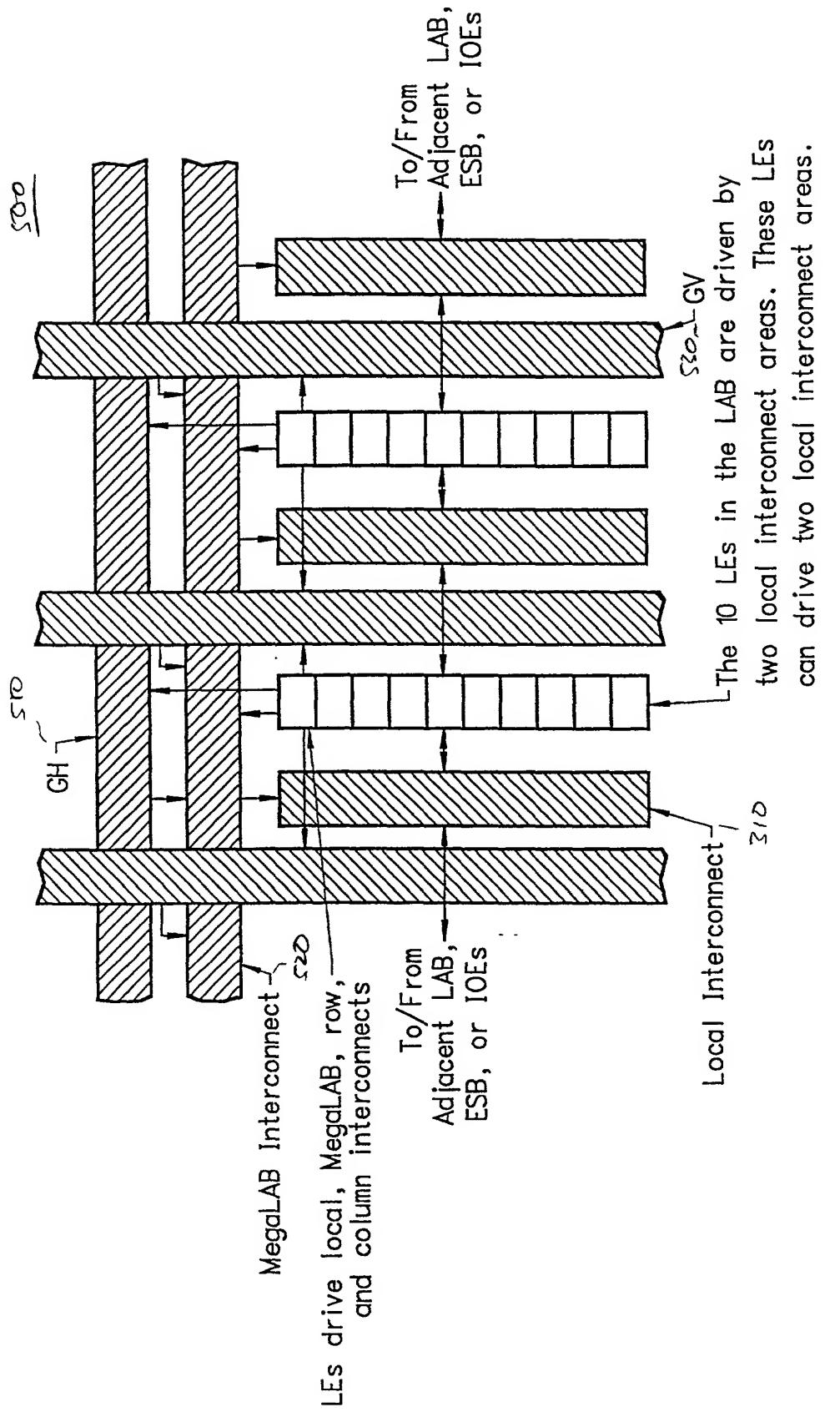
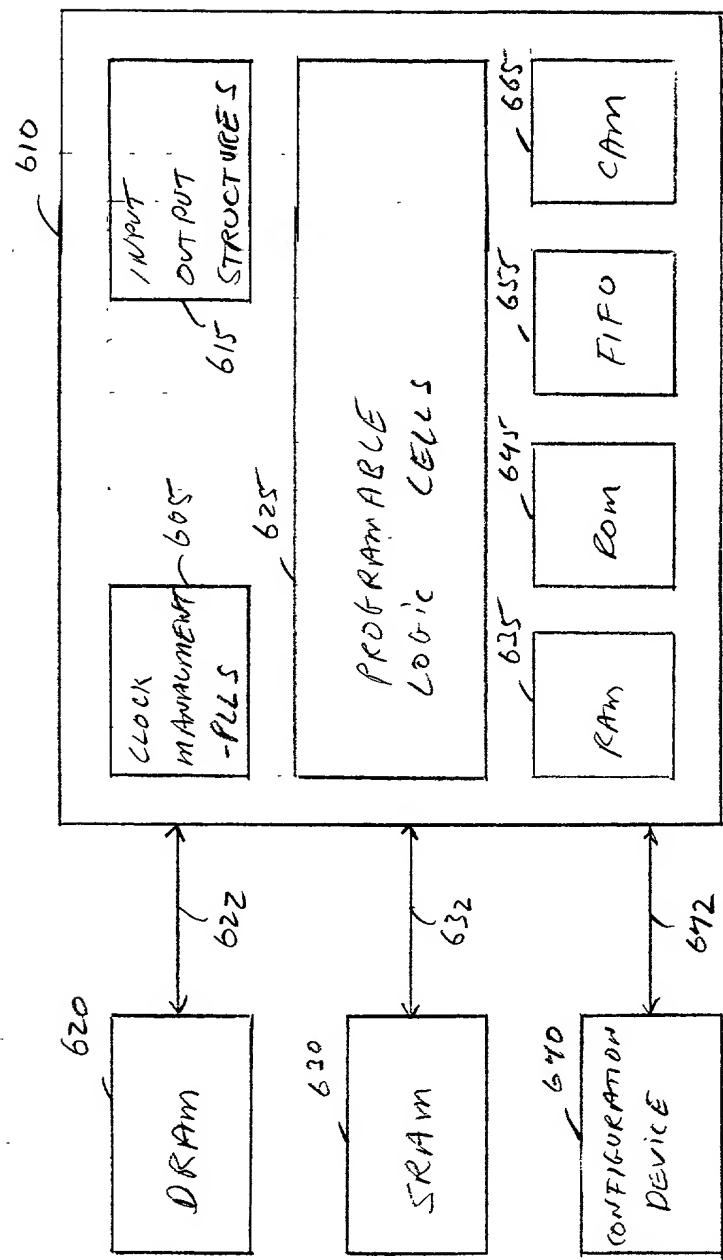


FIG. 5

600Figure 6

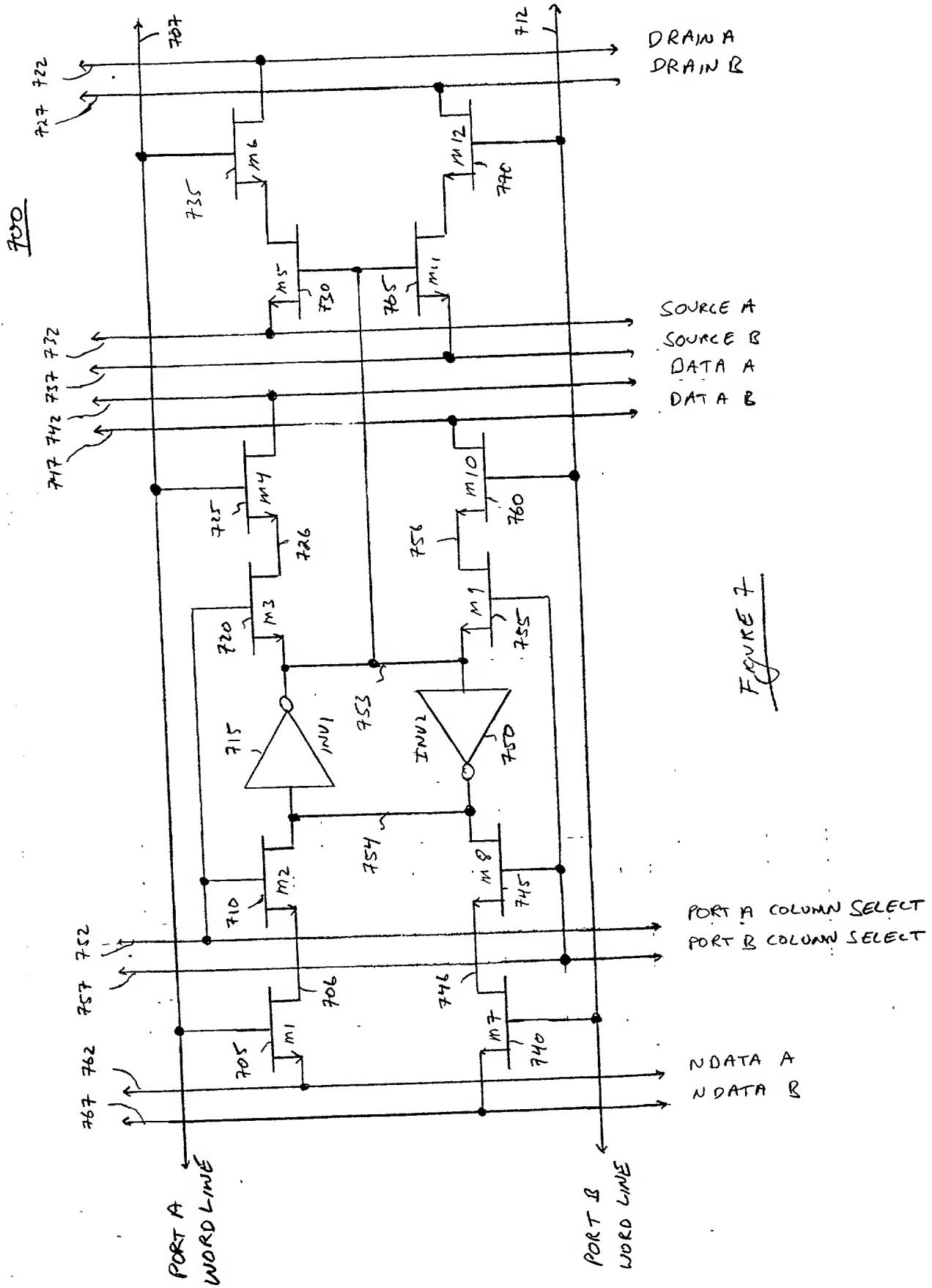


Figure 7

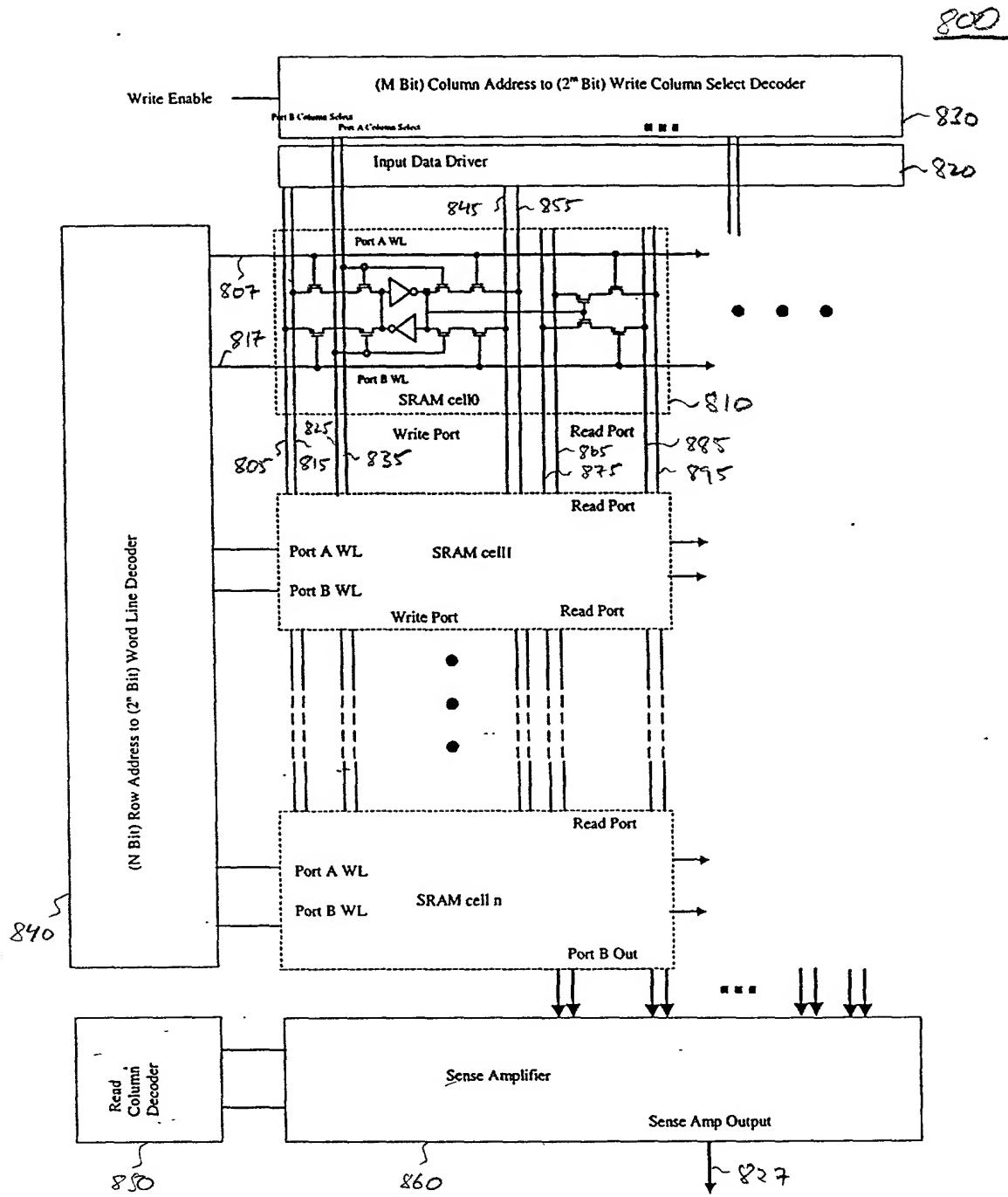


Figure 8

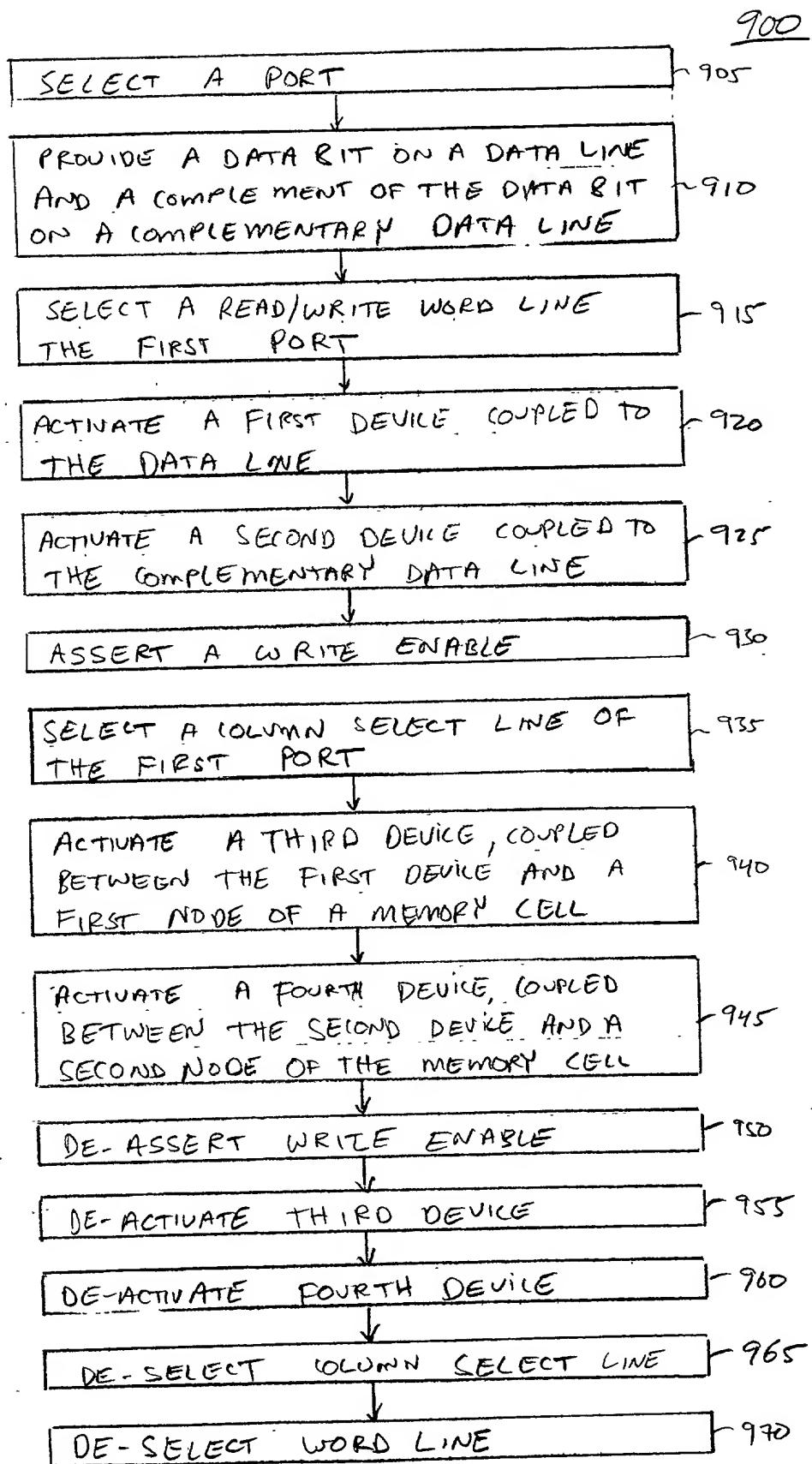


Figure 9

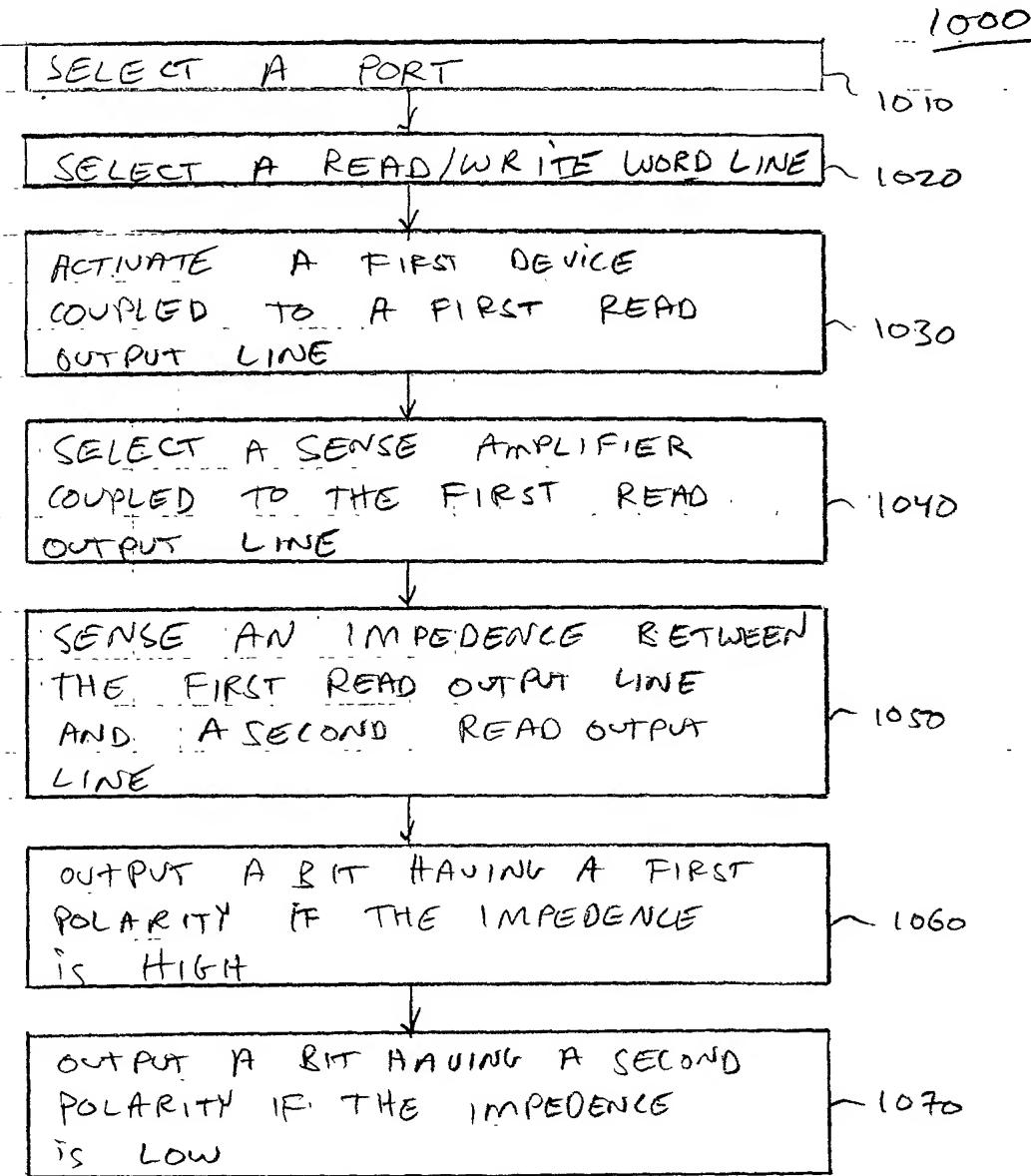


FIGURE 10

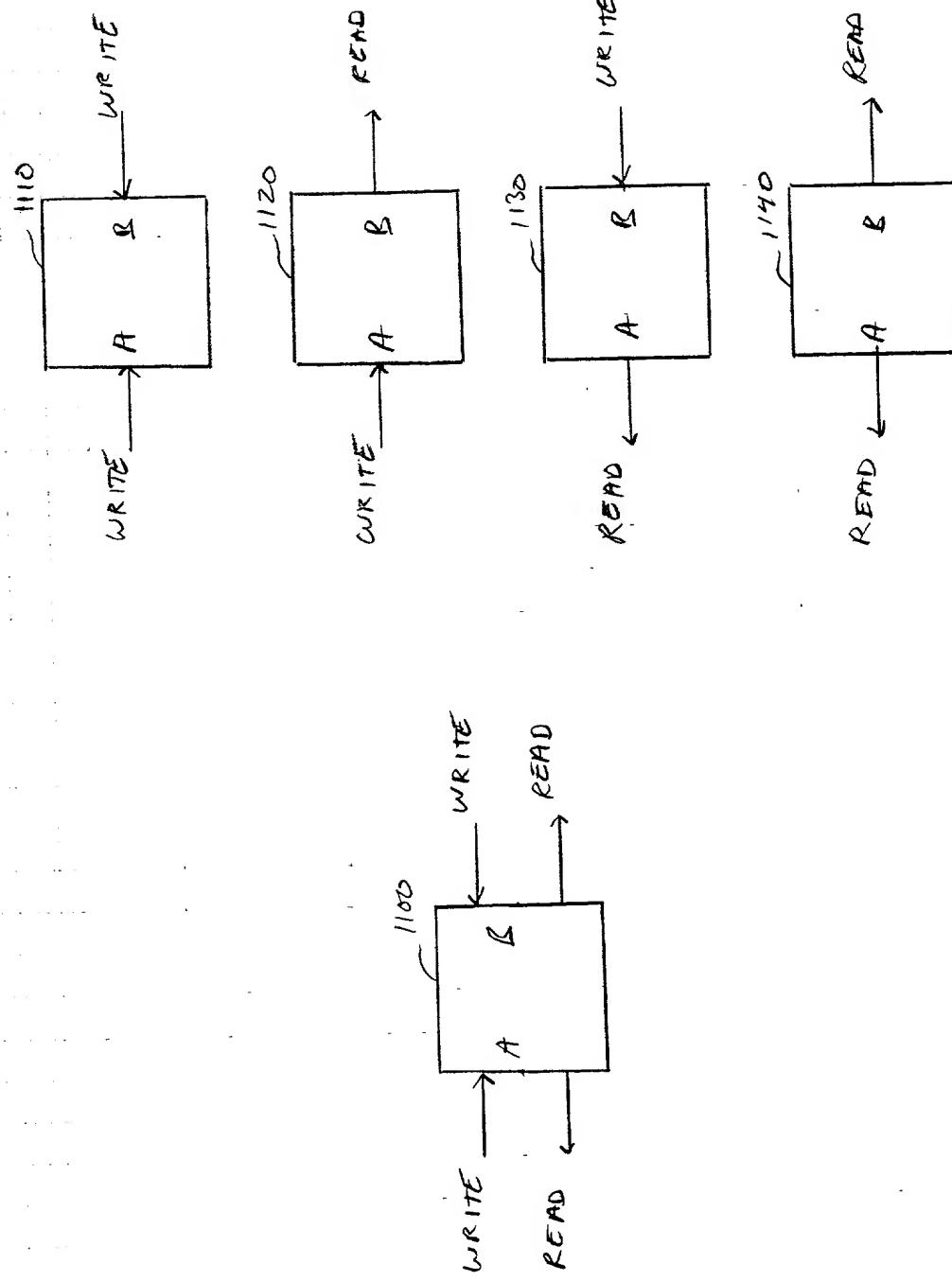


Figure 11

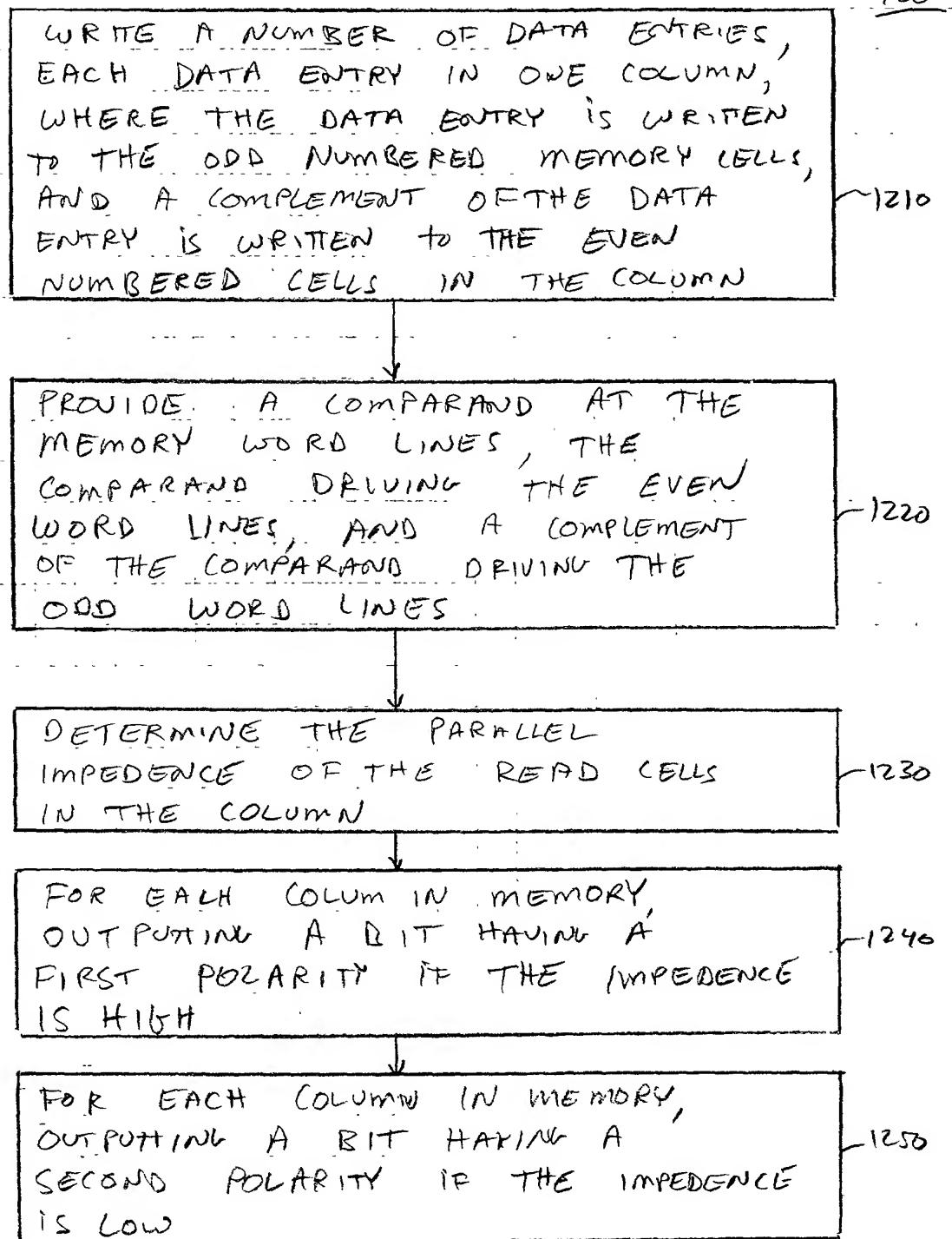
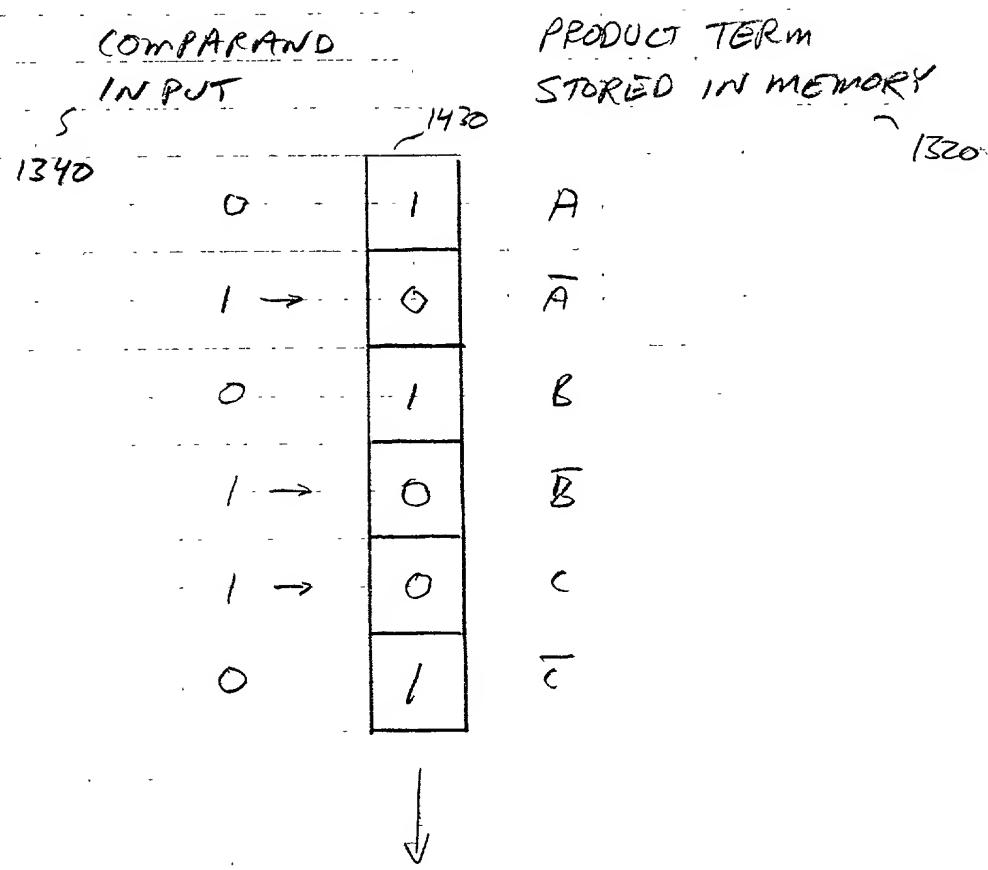


Figure 12

1300  
 PRODUCT TERM =  $A \cdot B \cdot \bar{C}$  <sup>1305</sup>

5 A "MATCH" OCCURS WHEN  $A=1, B=1, C=0$   
1310



HIGH IMPEDENCE AT  $\sim 1350$   
 SENSE AMPLIFIER  
 INPUT

FIGURE 18

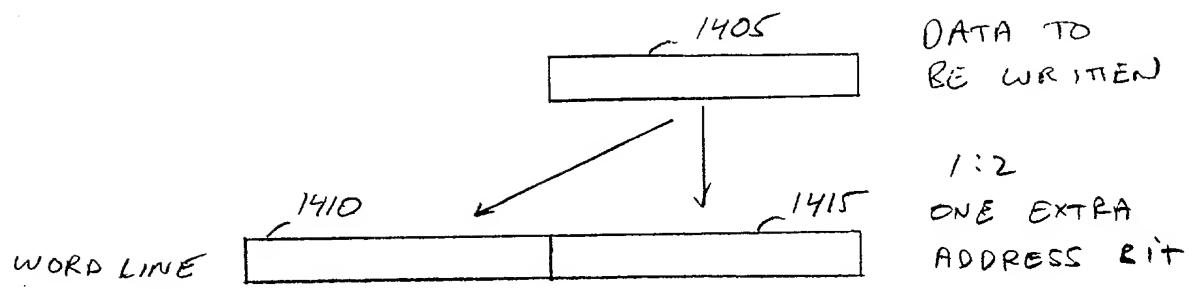


FIGURE 14A

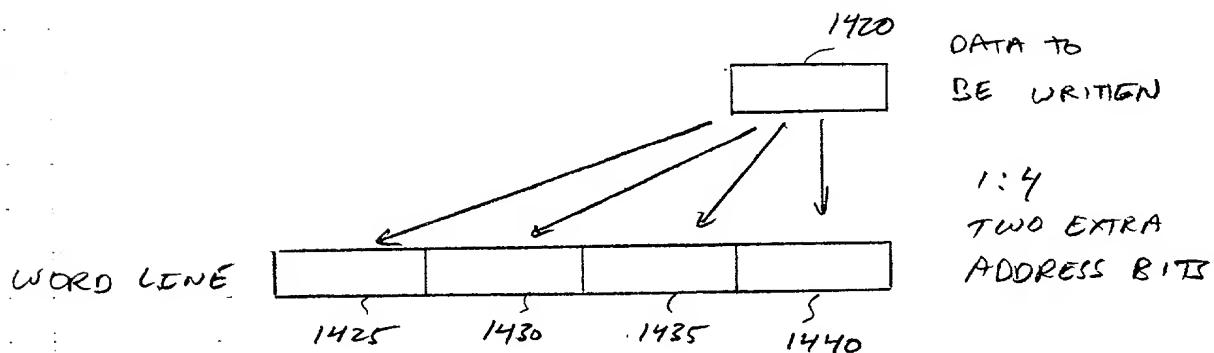


FIGURE 14 B

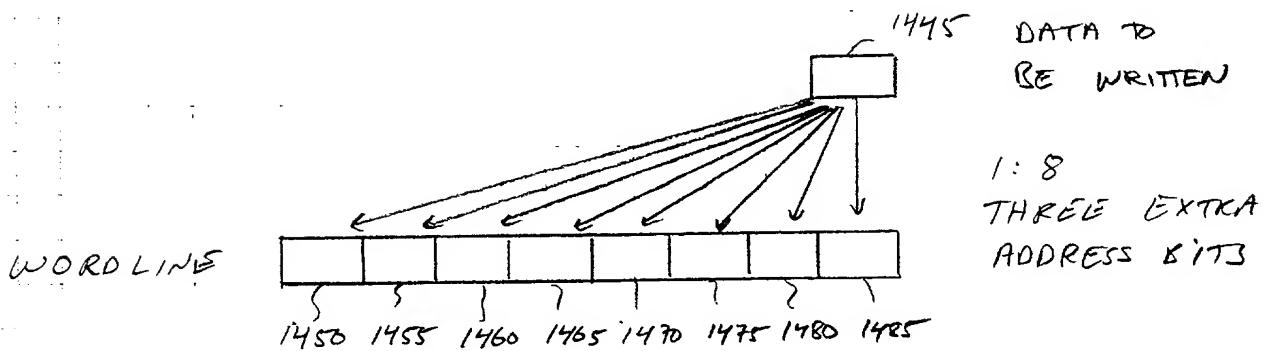
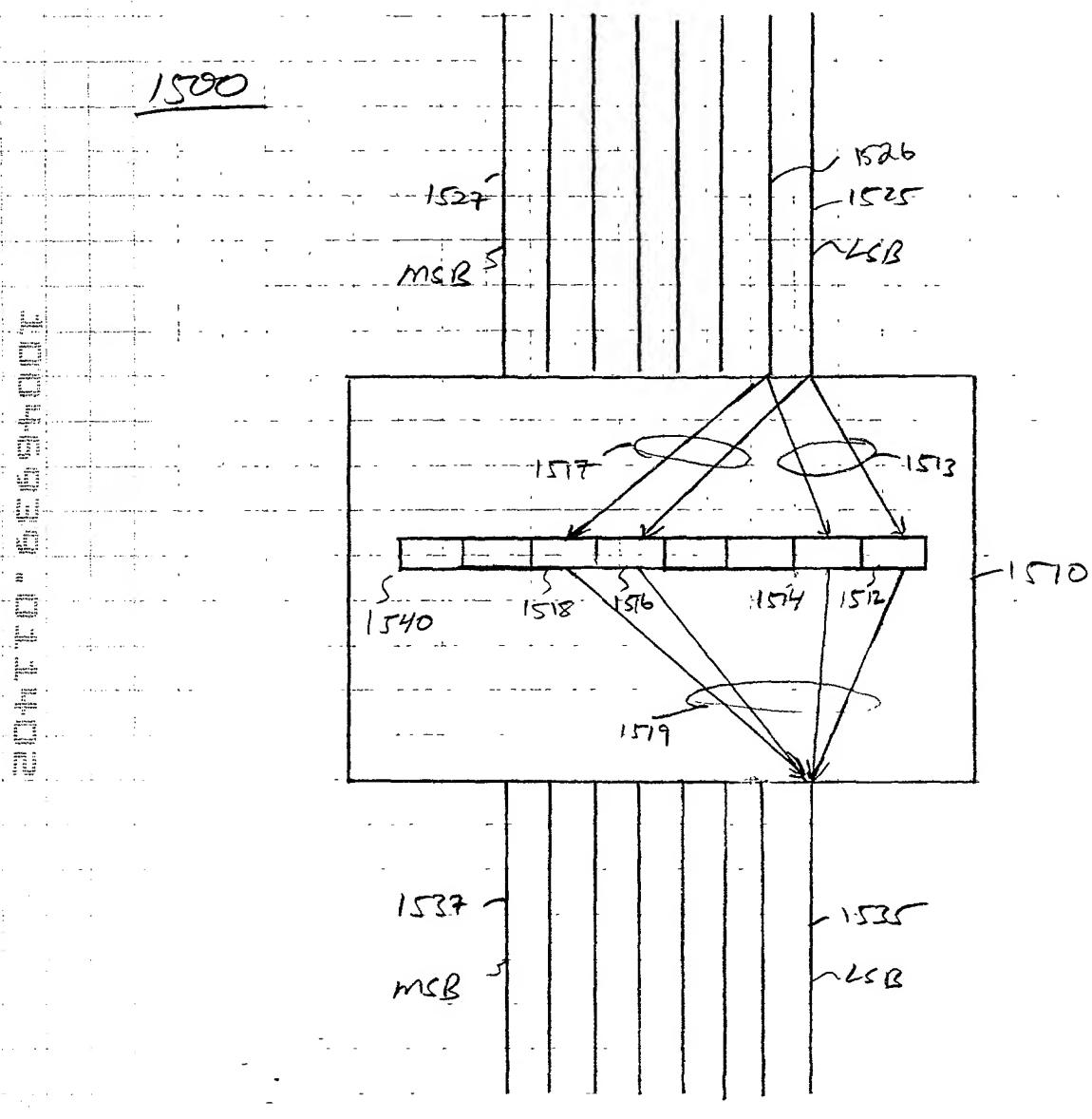


FIGURE 14 C

INPUT DATA BUS ~ 1520



OUTPUT DATA BUS ~ 1530

FIGURE 15

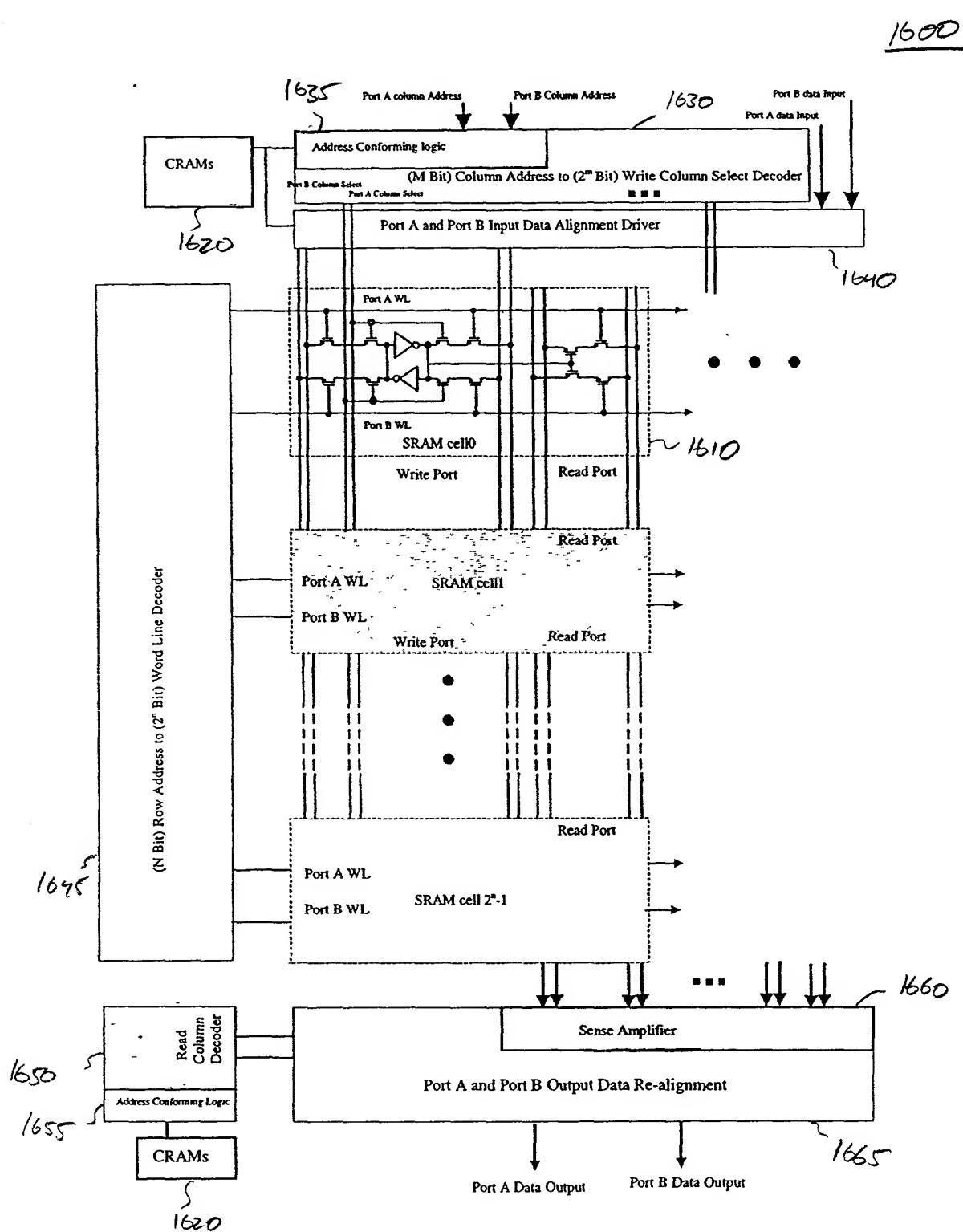


FIGURE 16

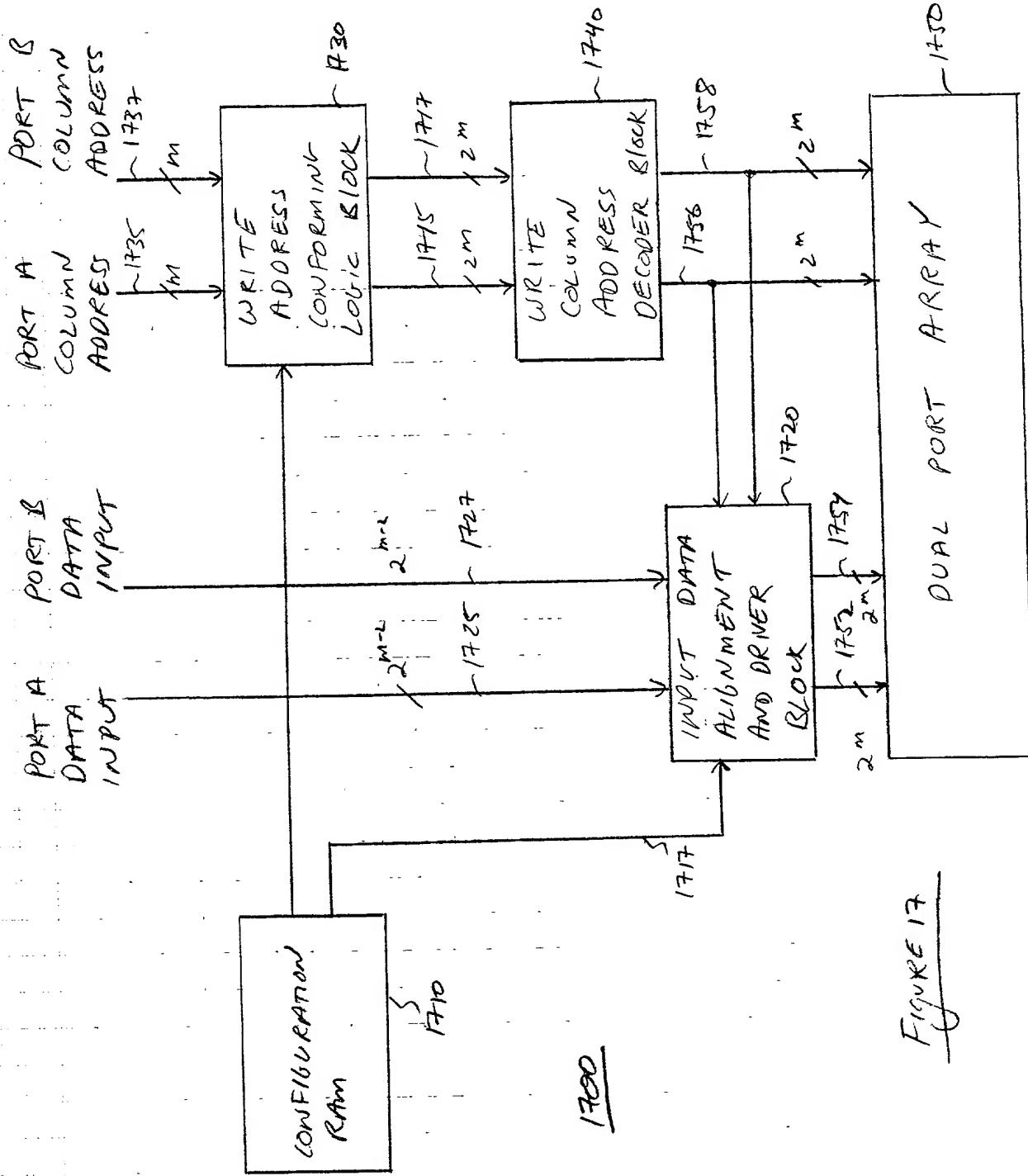


Figure 17

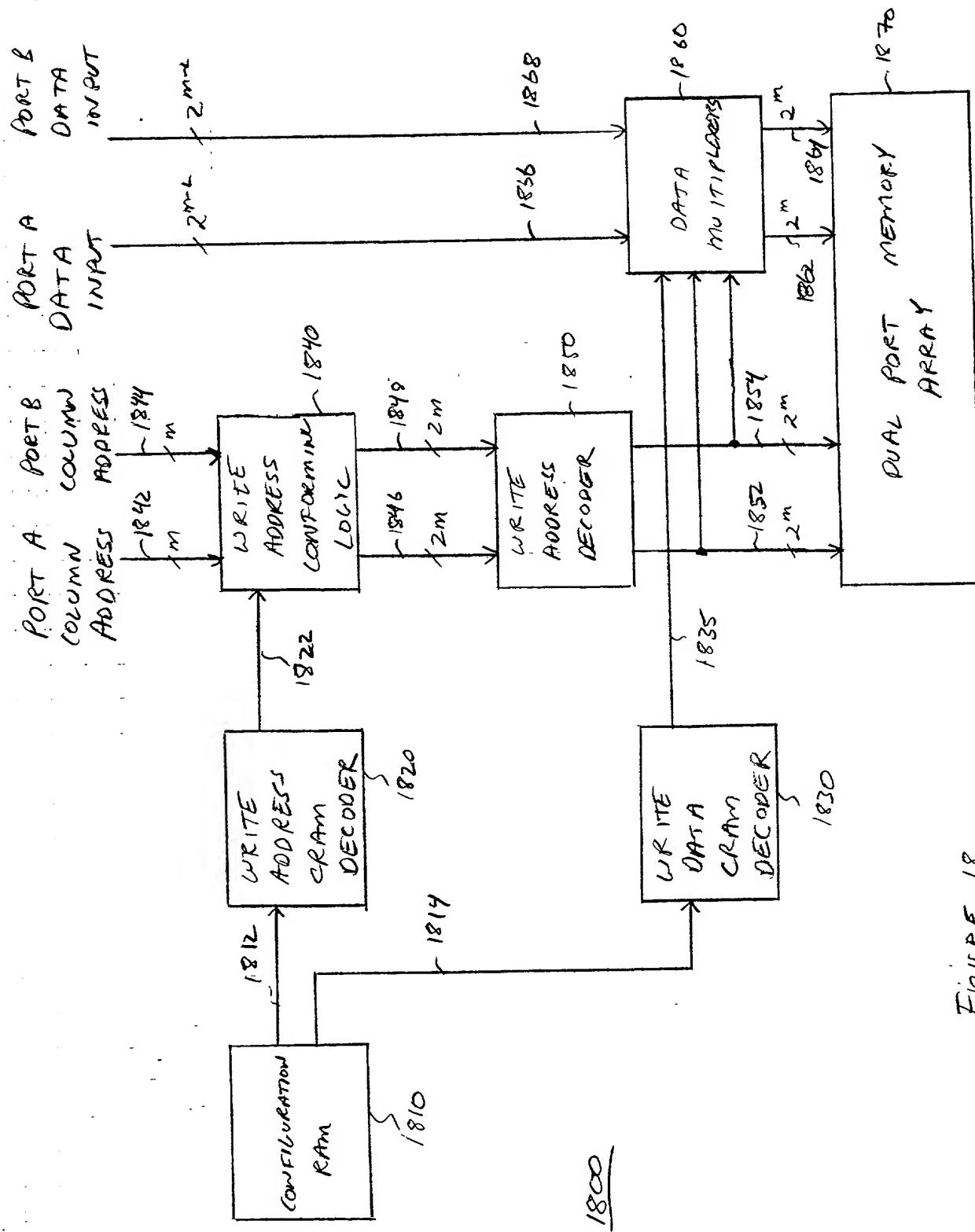


Figure 18

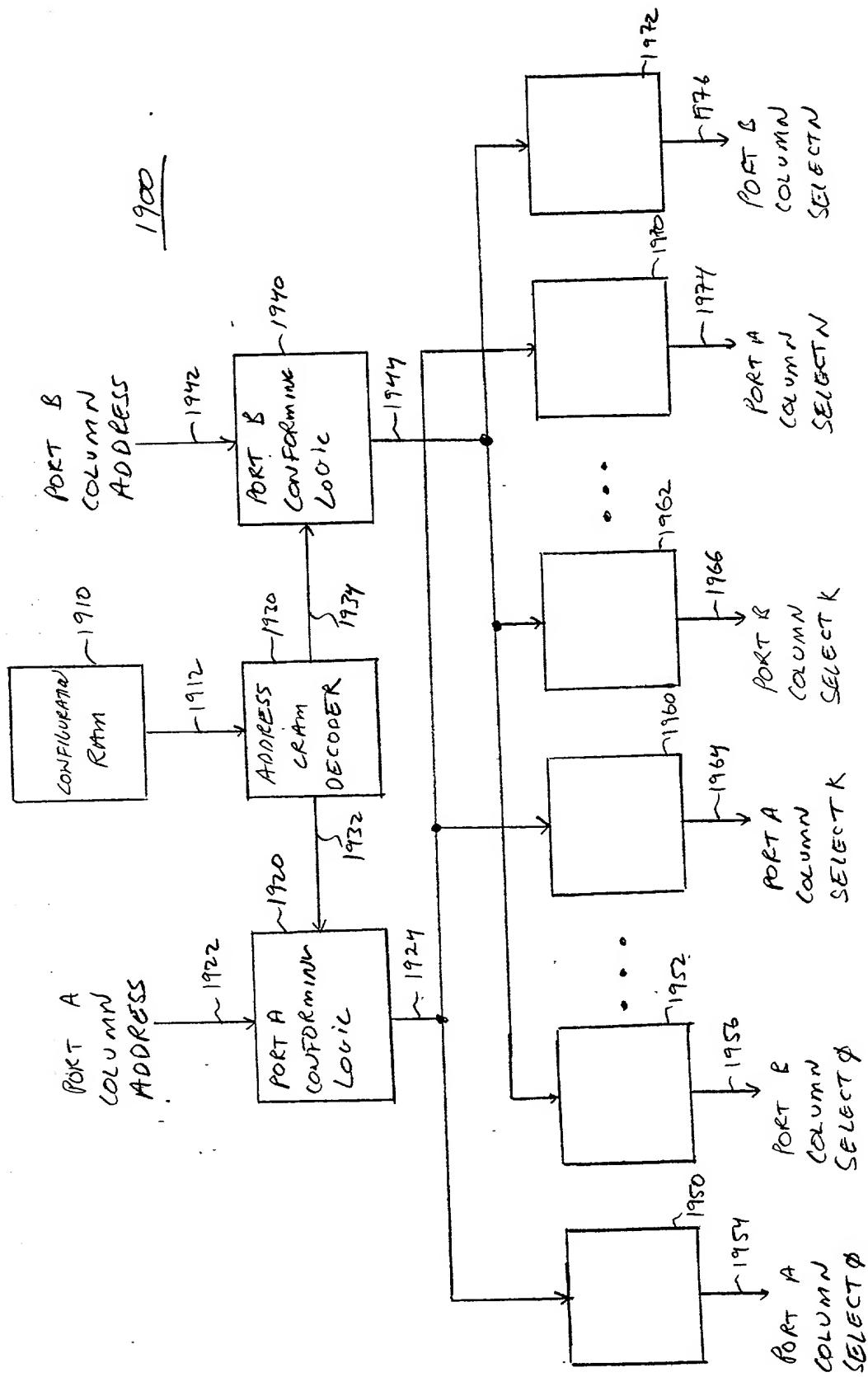


Figure 19

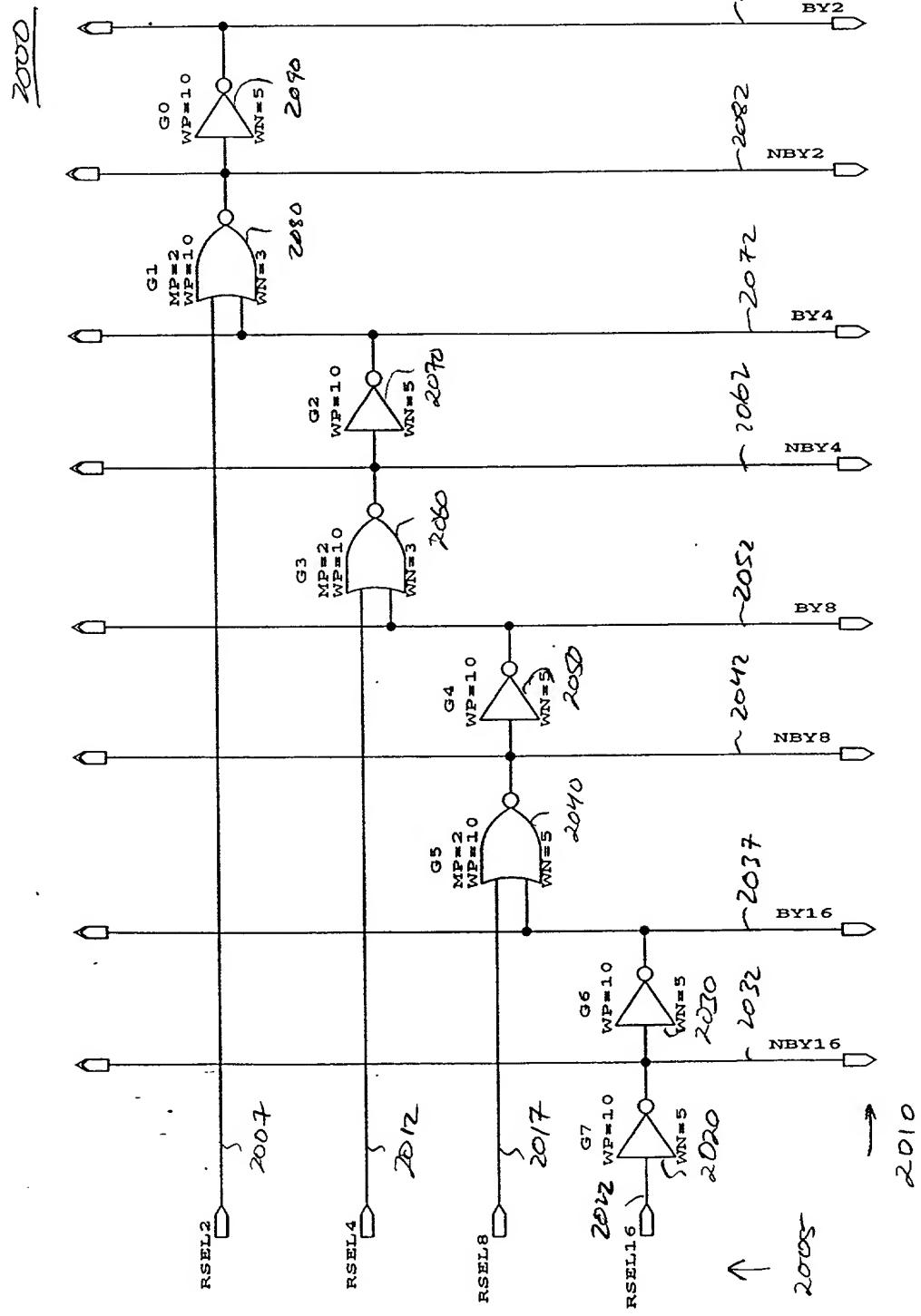


Figure 20

2100

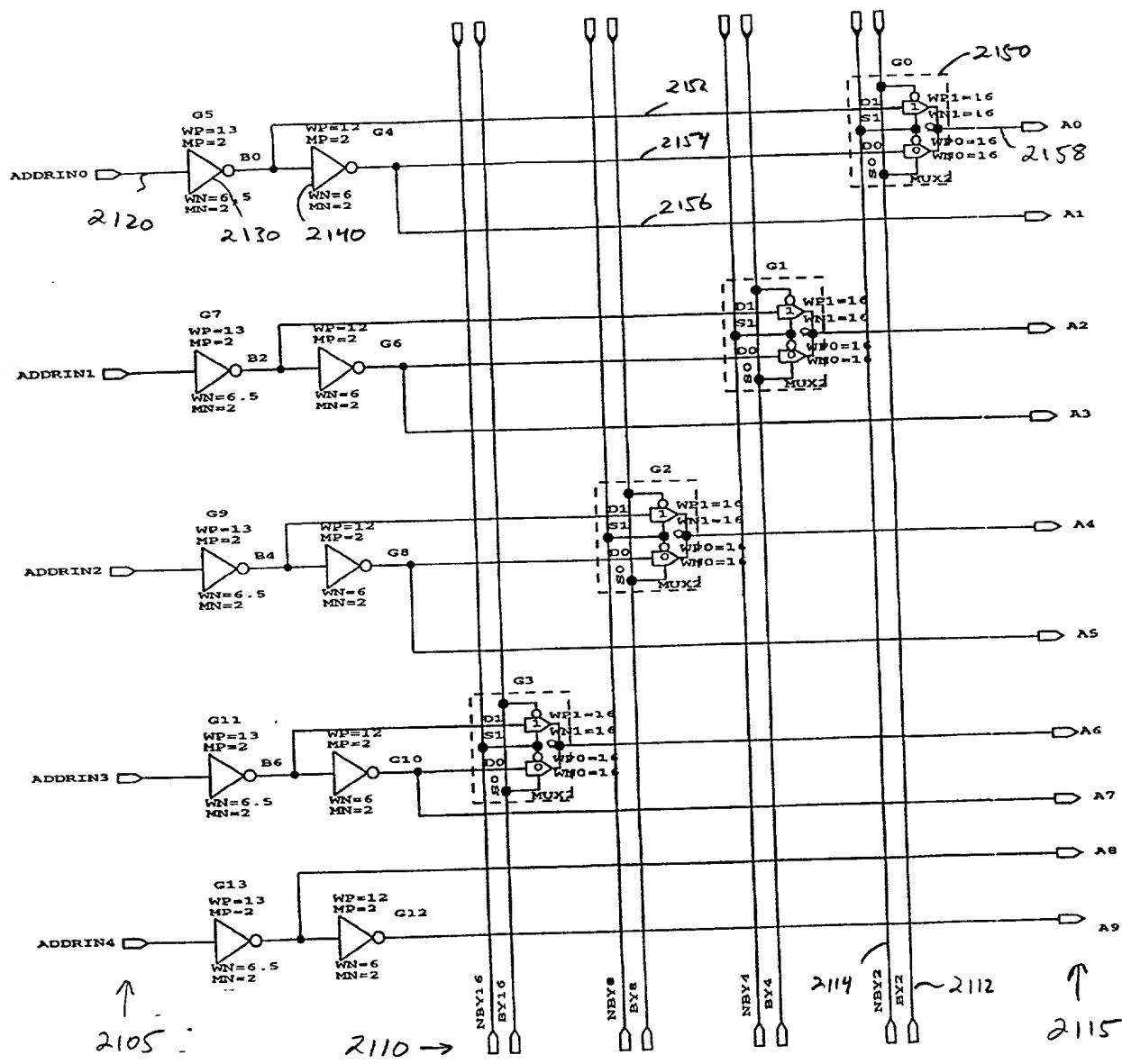
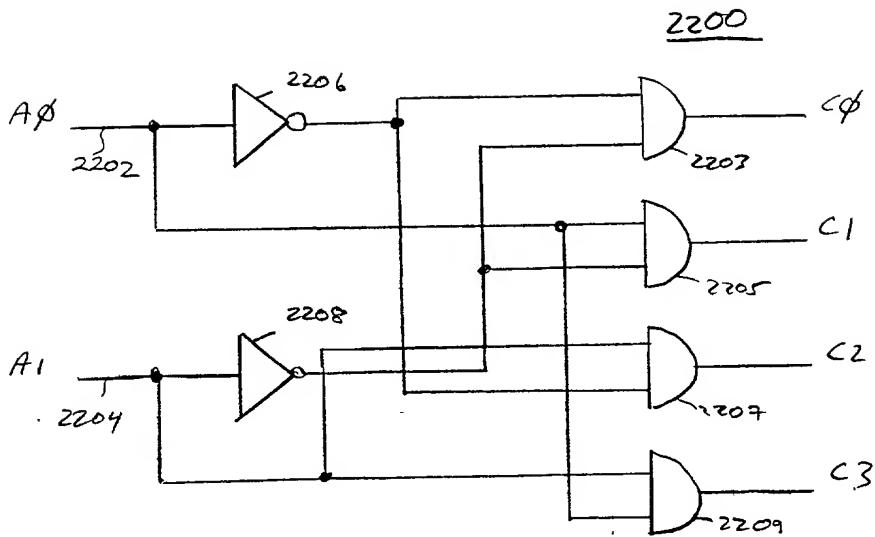


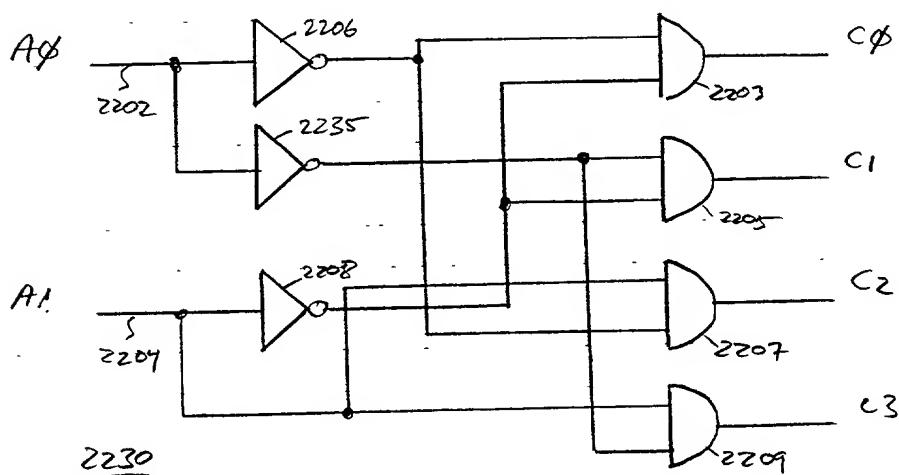
Figure 2)



$A_1$	$A_0$	$C_0$	$C_1$	$C_2$	$C_3$
0	0	1	0	0	0
0	1	0	1	0	0
1	0	0	0	1	0
1	1	0	0	0	1

2220

$$\begin{aligned}
 C_0 &= \overline{A_1} \cdot \overline{A_0} \\
 C_1 &= \overline{A_1} \cdot A_0 \\
 C_2 &= A_1 \cdot \overline{A_0} \\
 C_3 &= A_1 \cdot A_0
 \end{aligned}$$



2240

$$\begin{aligned}
 C_0 &= \overline{A_1} \cdot \overline{A_0} \\
 C_1 &= \overline{A_1} \cdot \overline{A_0} \\
 C_2 &= A_1 \cdot \overline{A_0} \\
 C_3 &= A_1 \cdot \overline{A_0}
 \end{aligned}$$

$A_1$	$A_0$	$C_0$	$C_1$	$C_2$	$C_3$	
0	0	1	1	0	0	$\sim 2252$
0	1	0	0	0	0	$\sim 2254$
1	0	0	0	1	1	$\sim 2256$
1	1	0	0	0	0	$\sim 2258$

FIGURE 22

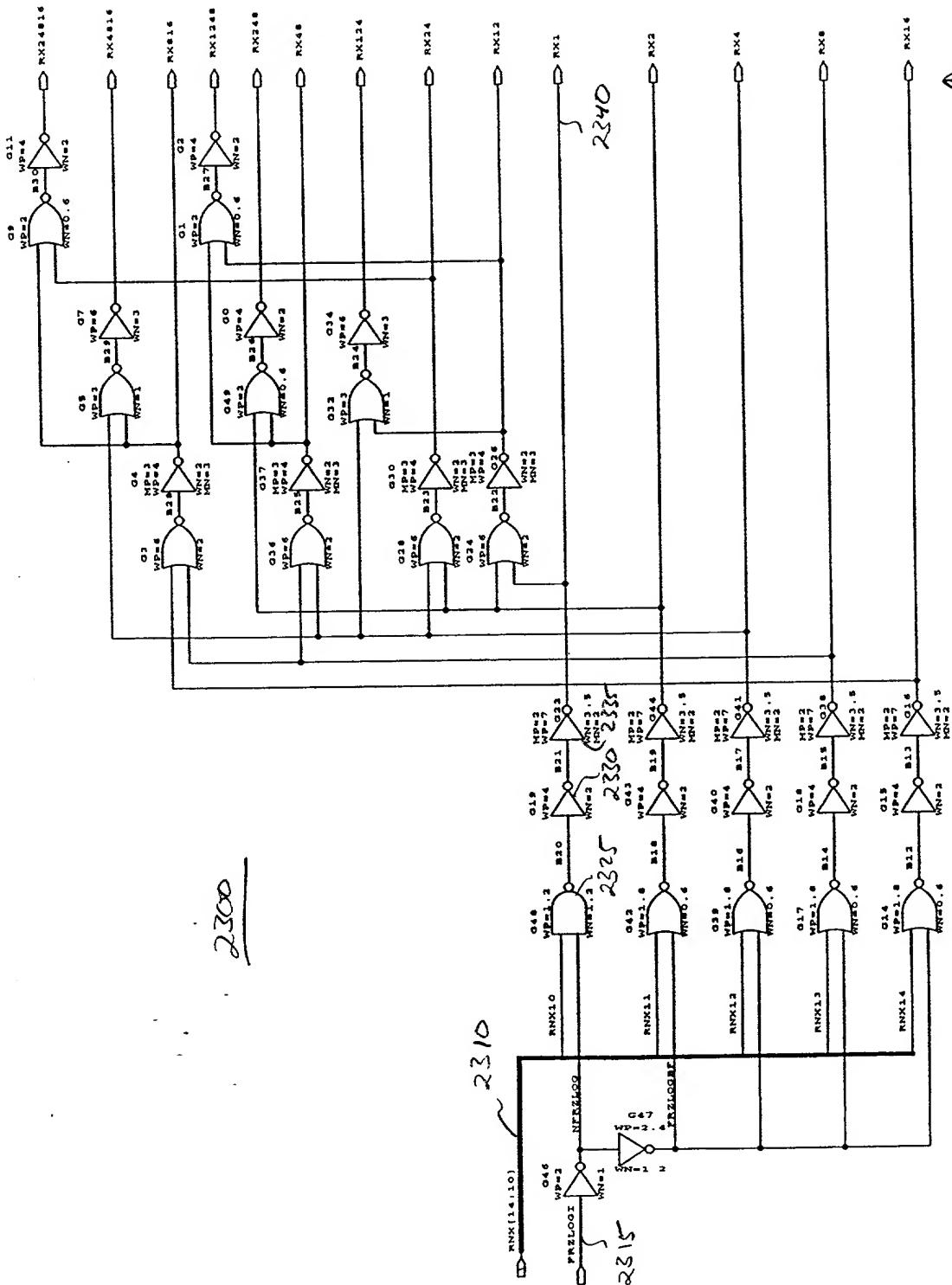


Figure 23

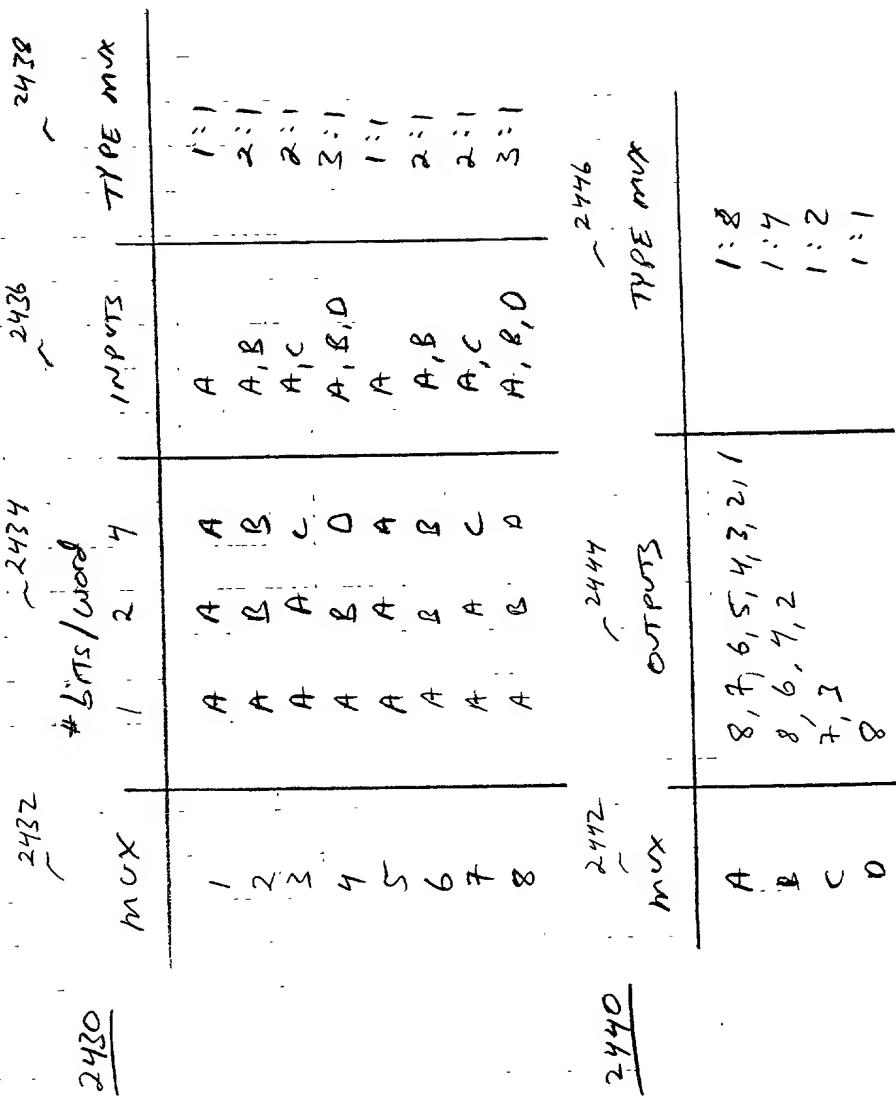
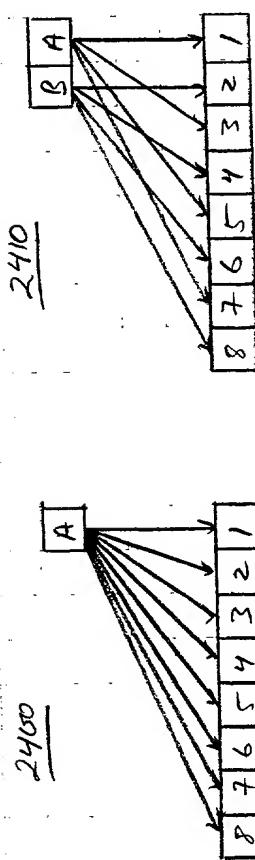
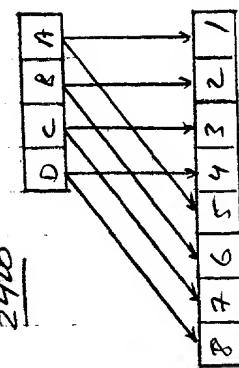


Figure 24

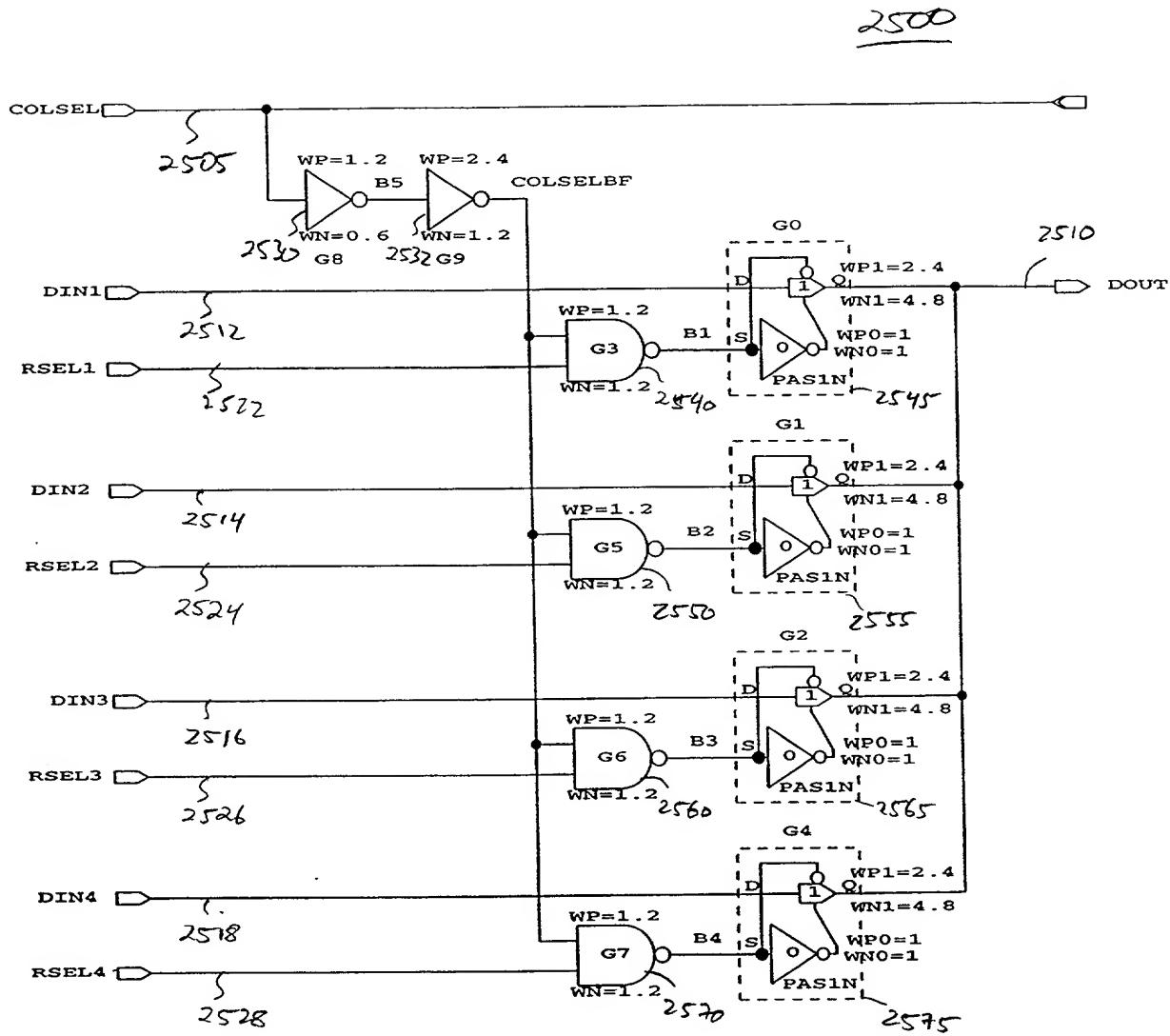


FIGURE 25

2600

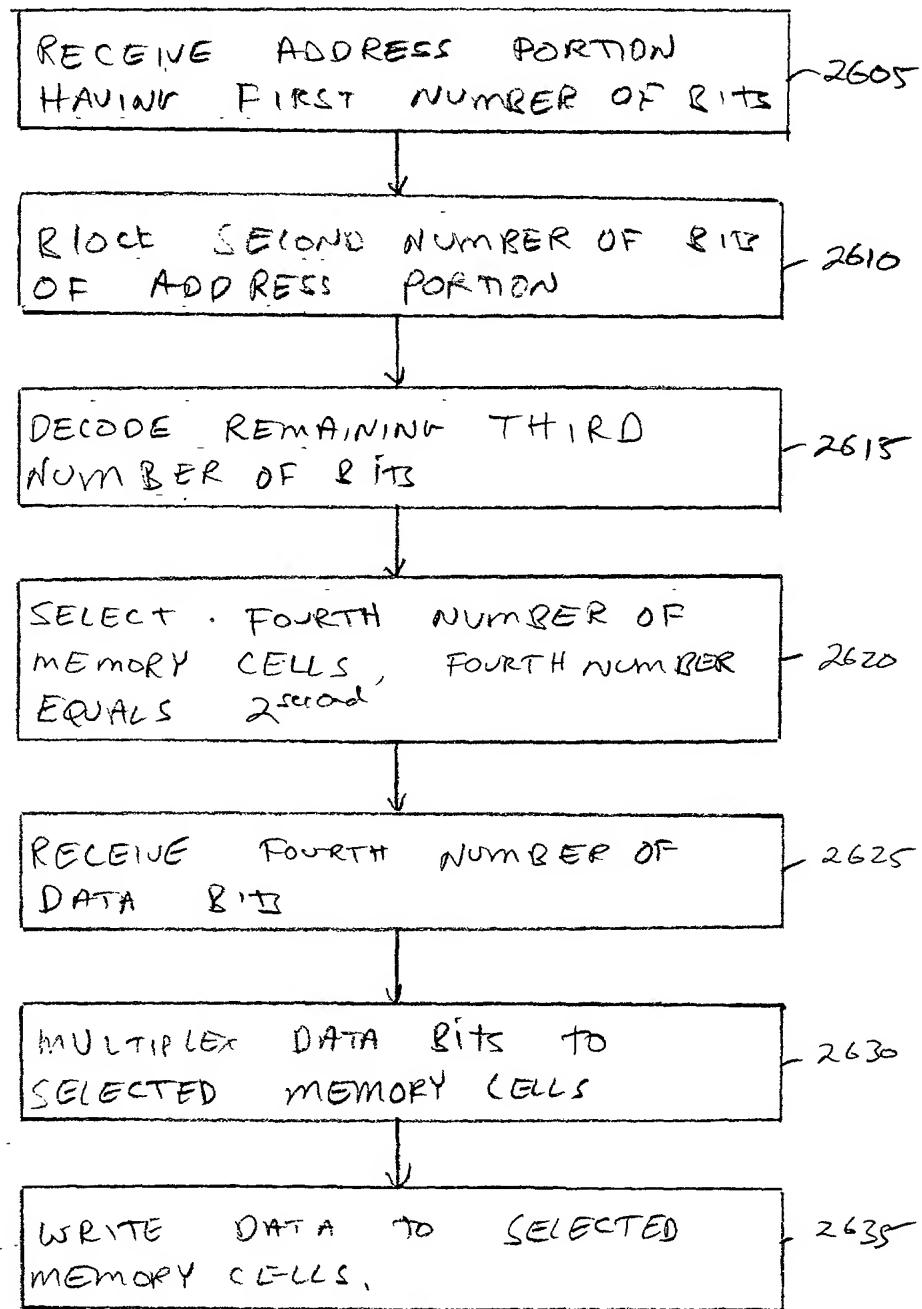


FIGURE 26

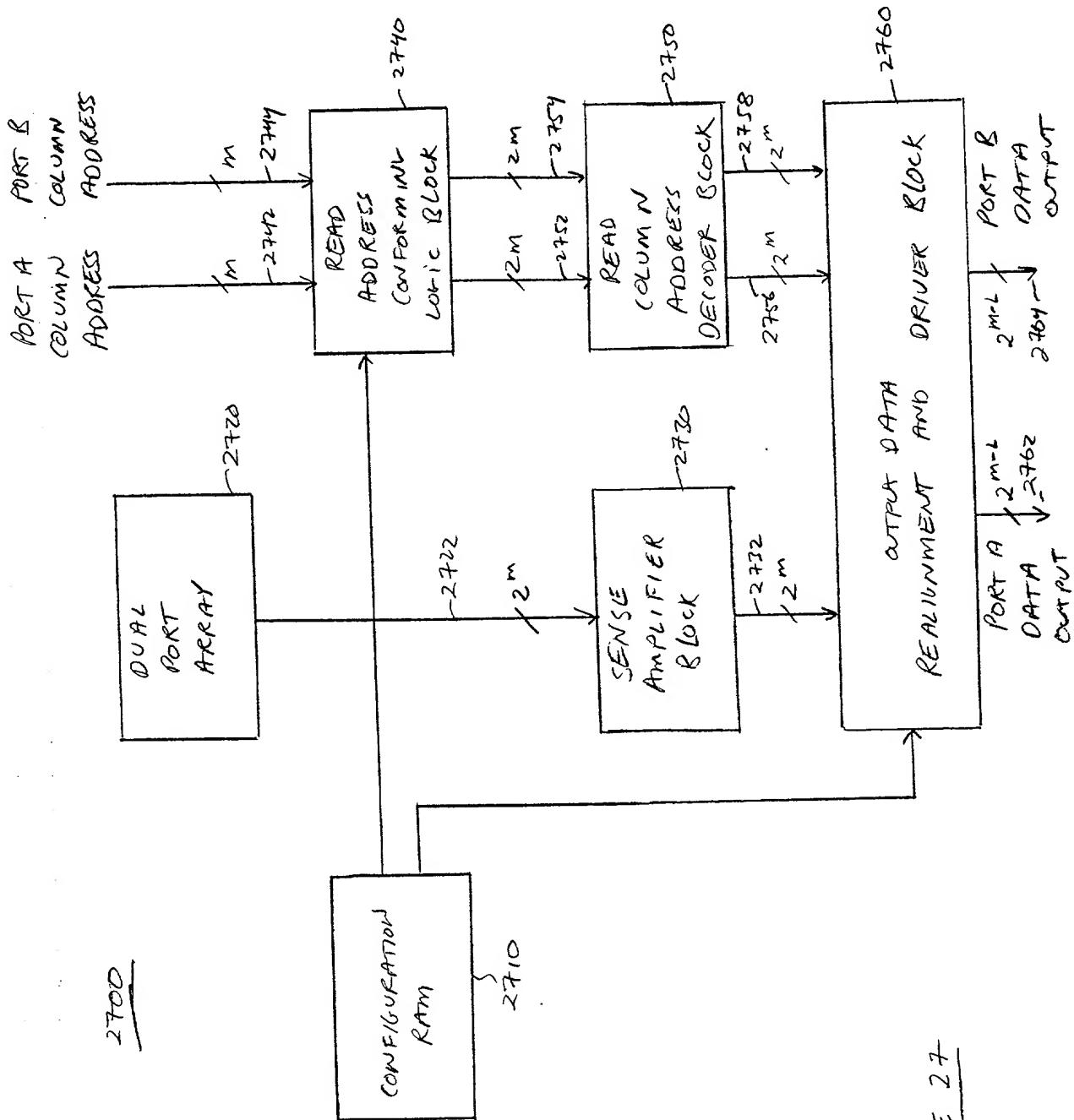


Figure 27

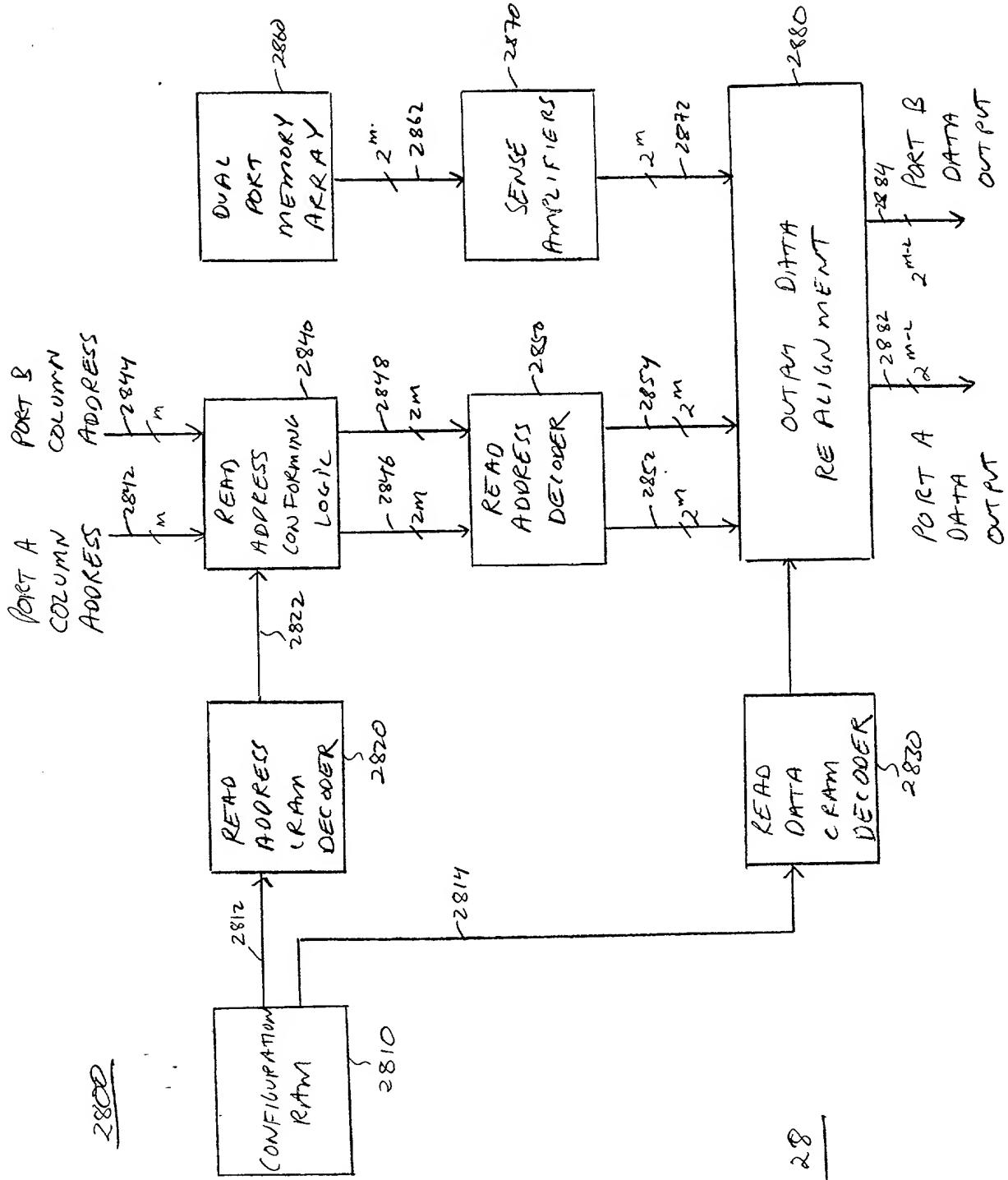


Figure 28

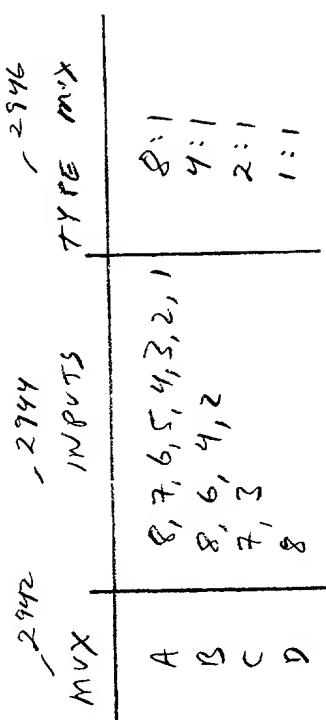
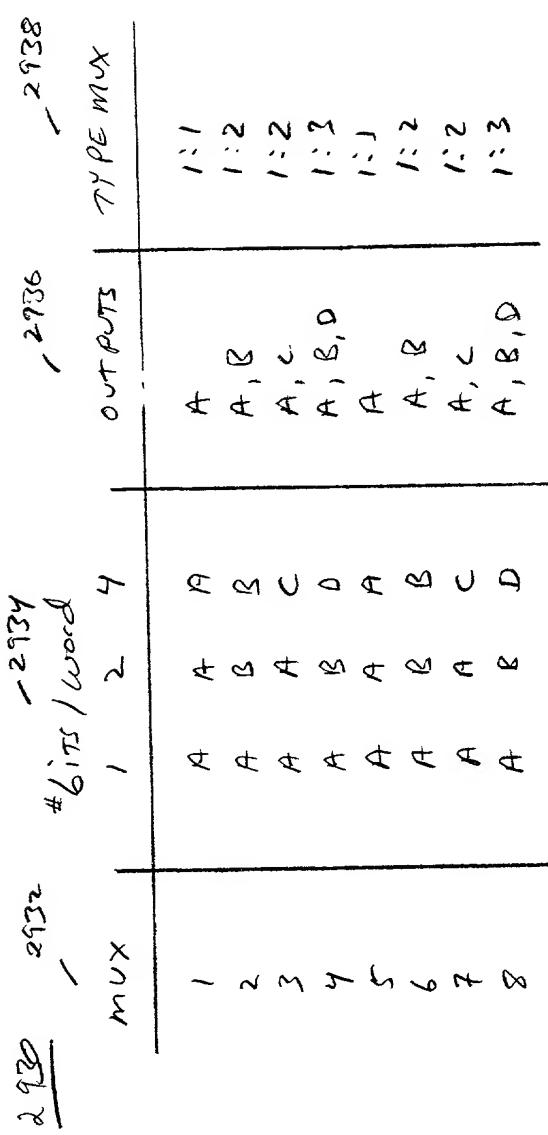
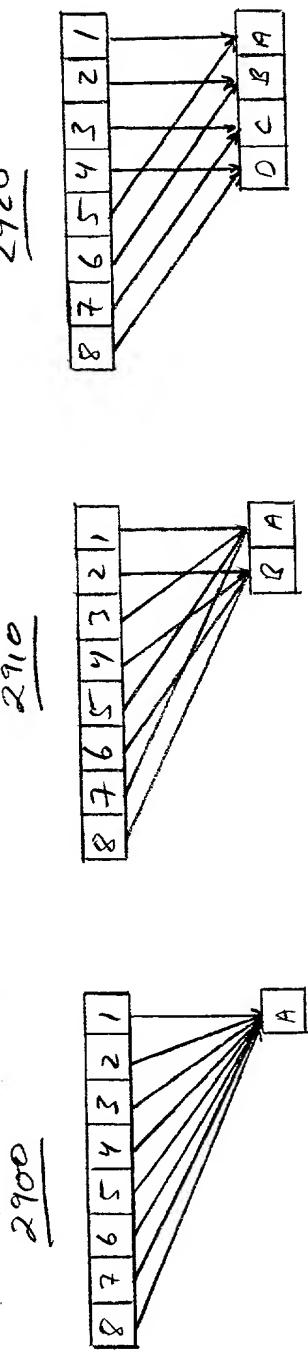


FIGURE 29

3005

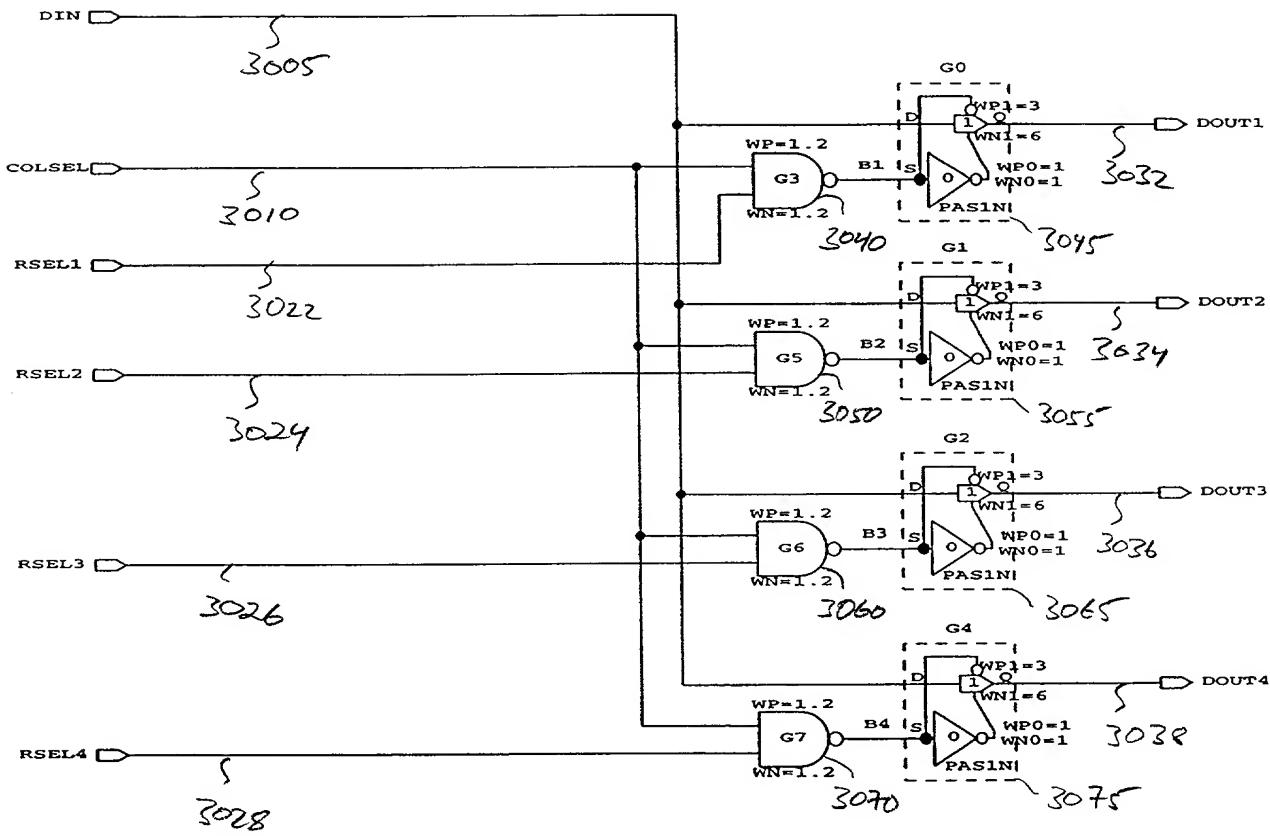


FIGURE 30

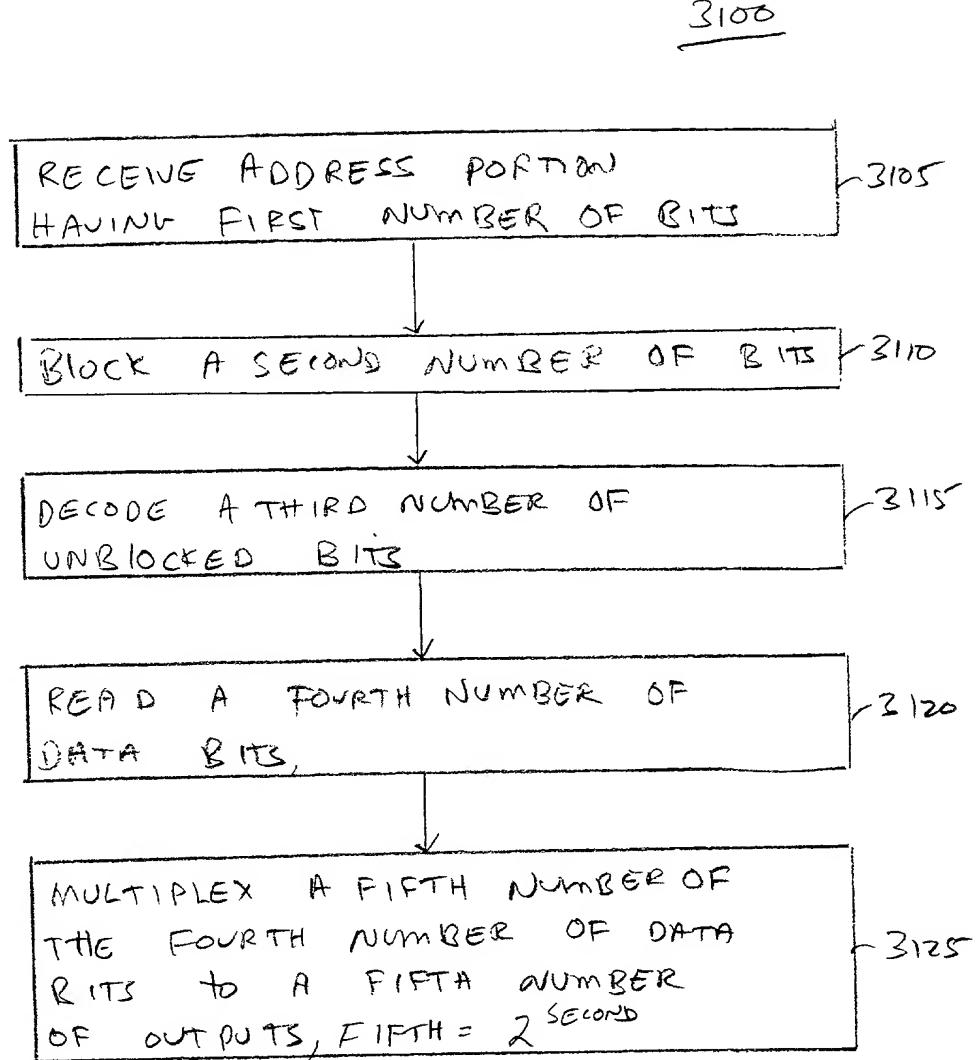


Figure 31